IN THE U.S. PATENT AND TRADEMARK OFFICE

pplication of

Docket No.: **TI-33175**

Robert B. Staszewski, et al.

Art Unit:

2637

Serial No.: 10/008,462

Examiner:

Corrielus, Jean B

Filed: 11/30/2001

Conf. No.:

5009

For: FREQUENCY SYNTHESIZER WITH PHASE RESTART

AMENDMENT TRANSMITTAL FORM

Commissioner for Patents

Alexandria, VA 22313-1450

Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. ≥1.8(a)

I hereby certify, that on this date, this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450 on

Elizabeth Austin

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated as shown below:

CLAIMS AS AMENDED							
	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA		RATE	ADDITIONAL FEE
Total Claims	18	Minus	20	= ()	x \$ 50 =	\$ 0.00
Independent Claims	2	Minus	3	= ()	X \$200 =	\$ 0.00
TOTAL ADDITIONAL FEE FOR THIS AMOUNT							\$ 0.00

Charge the total additional fee, and any further fees, or credit overpayment to deposit account no. 20-0668. An original and two copies of this sheet are enclosed.

Texas Instruments Incorporated P. O. Box 655474, M/S 3999 Dallas, TX 75265 (972) 917-5299

Ronald O. Neerings Attorney for Applicants

Reg. No.: 34,227

Application No. 10/008,462 Amendment dated August 31, 2005 Reply to Office Action of May 31, 2005



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LETTER PROPOSING DRAWING AMENDMENT UNDER RULE 123

Commissioner for Patents

Alexandria, VA 22313-1450

Elizabeth Austin

MAILING CERTIFICATE UNDER § 37 CFR 1.8(a)

the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents and Trademarks, Alexandria,

I hereby certify that this correspondence is being deposited with

Dear Sir:

Responsive to the Office Action of May 31, 2005, Applicants propose amending Figure 12a to clarify the invention. Applicants propose:

VA 22313-1450 on

- 1) adding a signal line from the output of DCO 12, which outputs differential clock signal CKV, to an input of Phase Detection Circuit 60. Support for this amendment is found in paragraph [0033] and Figure 1.
- 2) adding a signal line, coupled to receive a signal CTL SRST, to an input of Phase Detection Circuit 60. Support for this amendment is found in paragraph [0056] and Figures 13 & 14 which show synchronous reset CTL SRST.

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3) adding a signal line, enabled to carry a mode switch signal OA_ZPR, from an output of control circuit 26 to an input of Phase Detection Circuit 60. Support for this

amendment is found in paragraphs [0066-0067] and Figure 14.

4) adding a signal line, enabled to carry a signal OP_ZPR, from an output of

control circuit 24 to an input of Phase Detection Circuit 60. Support for this amendment is

found in paragraphs [0065 & 0067] and Figure 14.

5) adding a signal line, coupled to receive a signal CTL_MODE_P, to an input of

control circuit 24. Support for this amendment is found in paragraphs [0064-0065] and

Figure 13.

6) adding a signal line, coupled to receive a signal CTL_MODE_A, to an input of

control circuit 26. Support for this amendment is derived from Figure 13 which shows

CTL MODE P for the PVT oscillator controller or interface 24. This equivalency is

established in [0066]. The acquisition controller 26, as described in [0068], is significantly

identical to the controller 24, and has an equivalent mode switch signal. Accordingly,

Figure 12a has been amended to show the CTL MODE P and CTL MODE A signals

entering OP 24 and OP 26 circuits, respectively.

All proposed amendments to Claim 12a are illustrated in red ink. No new matter

has been added. Applicants respectfully request approval.

Respectfully submitted,

Ronald O. Neerings

Reg. No. 34,227

Attorney for Applicants

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of

Robert B. Staszewski, et al.

Serial No.: 10/008,462

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For: **FREQU**

Docket No.: **TI-33175**

Art Unit: 2637

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Examiner: Corrielus, Jean B

Conf. No.: 5009

FREQUENCY SYNTHESIZER WITH PHASE RESTART

LETTER TO THE OFFICIAL DRAFTS PERSON

Commissioner for Patents Attn: Official Drafts Person Alexandria, VA 22313-1450

Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450

Elizabeth Austin

Submitted herewith are 10 sheets of formal drawings.

The enclosed drawings are being provided in a timely manner therefore no additional fee is required.

No fee is believed by Applicant to be due; however, the Commissioner is hereby authorized and requested to charge any necessary fee to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,

An O. Nung

Texas Instruments Incorporated

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Dallas, Texas 75265

(972) 917-5299

Ronald O. Neerings

Attorney for Applicants

Reg. No.: 34,227